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[Home](#)

 $\succ$

A diagram of a 5-bit shift register. It consists of five rectangular cells connected in a horizontal line. The top input of the first cell is labeled 'A', the top input of the second is 'B', the top input of the third is 'C', the top input of the fourth is 'D', and the top input of the fifth is 'E'. The bottom output of the first cell is labeled 'A', the bottom output of the second is 'B', the bottom output of the third is 'C', the bottom output of the fourth is 'D', and the bottom output of the fifth is 'E'. The cells are connected such that the output of one cell is the input of the next cell to its right.

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- [illegible]

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2020-08-20

[illegible]

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2020-08-17

በጣም ጥሩ የ iPhone ቢሆንም በጣም ብዙ የሚሰጠው 3 ዓመት የጥገና ምርት ሲሆን በጣም ጥሩ የሆኑ ሲሆኑ በጣም የሚሰጠው 2019 ዓ.ም. የሆኑ ሲሆኑ በጣም ጥሩ የሆኑ ሲሆኑ.

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2020-08-15

[illegible]

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2020-08-15

[illegible]

Email:NoTz_In39j@mail.com

2020-08-12

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